

TIGER[®] SHARC



TS-C43



Quad TigerSHARC PMC

- 6GFLOPS
- FPGA Co-Processor and I/O Engine
- Direct Digital I/O (FPDP, LVDS, Custom)
- Front Panel or PMC User I/O

TS-P36N



Quad TigerSHARC PCI

- 6-12GFLOPS
- FPGA Co-Processor and I/O Engine
- PMC Expansion Site (ADSP-TS101 DSPs, Analog I/O, etc.)
- Flexible Link Port Routing Modules

TS-V39



Octal TigerSHARC VME/cPCI

- 6-12GFLOPS
- FPGA Co-Processors and I/O Engine
- Power-Up Link Port Configurations
- VME P0 Backplane Link Port Routing for System-Wide DSP Arrays

TS-Lib



TigerSHARC DSP Library

- Over 400 Hand Optimized Functions (FFTs, FIR Filters, Correlation, Convolution, Matrix, Vector, Statistical, etc.)
- Up to 400% Speed Increase Over Similar Libraries
- Consistent Calling Interface for Efficient Function Pipelining



TigerSHARC DSP Product Summary

Feature	TS-P36N	TS-C43	TS-V39	TS-CP39
DSP				
Type	250MHz ADSP-TS101	250MHz ADSP-TS101	250MHz ADSP-TS101	250MHz ADSP-TS101
Number	4 (expandable to 8)	4	4 or 8	4 or 8
Architecture	single cluster	single cluster	one or two clusters of four	one or two clusters of four
Link Ports				
Number	passive - see options below	two external (optional)	four front panel, four VME P0	four front panel, four cPCI P3
Link Port Routing	fully connected, 2D-array, double pipeline, full breakout	FPGA Routing (static on power-up)	FPGA Routing (static on power-up)	FPGA Routing (static on power-up)
Memory				
SDRAM	128Mbytes	128Mbytes	2x 128Mbytes	2x 128Mbytes
FLASH	4Mbytes	4Mbytes	2x 4Mbytes	2x 4Mbytes
Other				
FPGA	Xilinx Virtex-II XC2V1000/3000	Xilinx Virtex-II XC2V1000/3000	2x Xilinx Virtex-II XC2V1000/3000	2x Xilinx Virtex-II XC2V1000/3000
Interfaces				
PCI	64-bit/66MHz 2 DMA channels	64-bit/66MHz 2 DMA channels	64-bit/33MHz between DSP clusters, 2 DMA channels	64-bit/66MHz 2 DMA channels
VME	-	-	VME64 (Universe II)	-
FPGA	64-bit port	64-bit front panel, 64-bit PMC user I/O	2x 64-bit front panel, 2x 32-bit user I/O per cluster	2x 64-bit front panel, 2x 32-bit user I/O per cluster
Other	JTAG, FPDP, LVDS parallel	JTAG, FPDP, LVDS parallel	JTAG, FPDP, LVDS parallel	JTAG, FPDP, LVDS parallel
Software				
Libraries	TS-Lib (Signal & Image Processing), TDSP BSP	TS-Lib (Signal & Image Processing), TDSP BSP	TS-Lib (Signal & Image Processing), TDSP BSP	TS-Lib (Signal & Image Processing), TDSP BSP
Development Tools	VisualDSP++	VisualDSP++	VisualDSP++	VisualDSP++
Operating Systems	VSPWorks, OSE	VSPWorks, OSE, GEDAE	VSPWorks, OSE, GEDAE	VSPWorks, OSE
Host OS Run-Time Support	Windows NT/2000	Windows NT/2000, VxWorks	LynxOS, VxWorks	Windows NT/2000



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