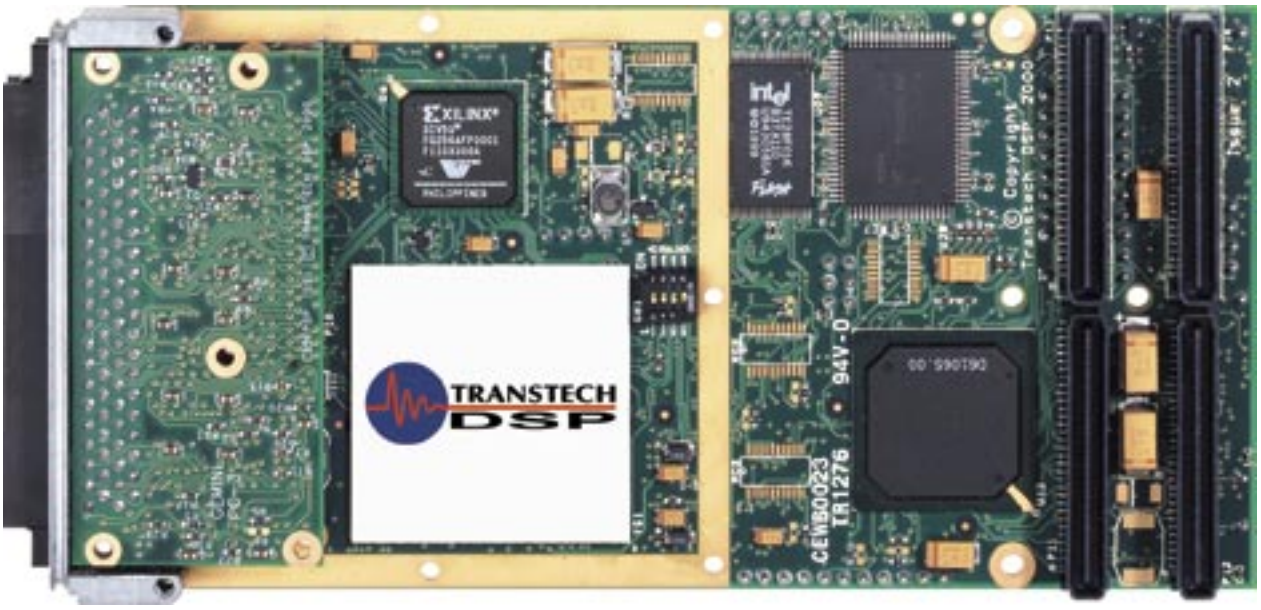
PMC-FPGA01

Digital I/O PMC
with Xilinx Virtex™ FPGA



Features

Xilinx XCV300 Virtex™ FPGA

Header module to provide level translation and external connector

Choice of front panel or user I/O routing

Programming FPGA clock

FPDP option

LVDS option

32/64-bit 33/66MHz master/slave PCI interface

128Mbytes SDRAM

2Mbytes SBSRAM

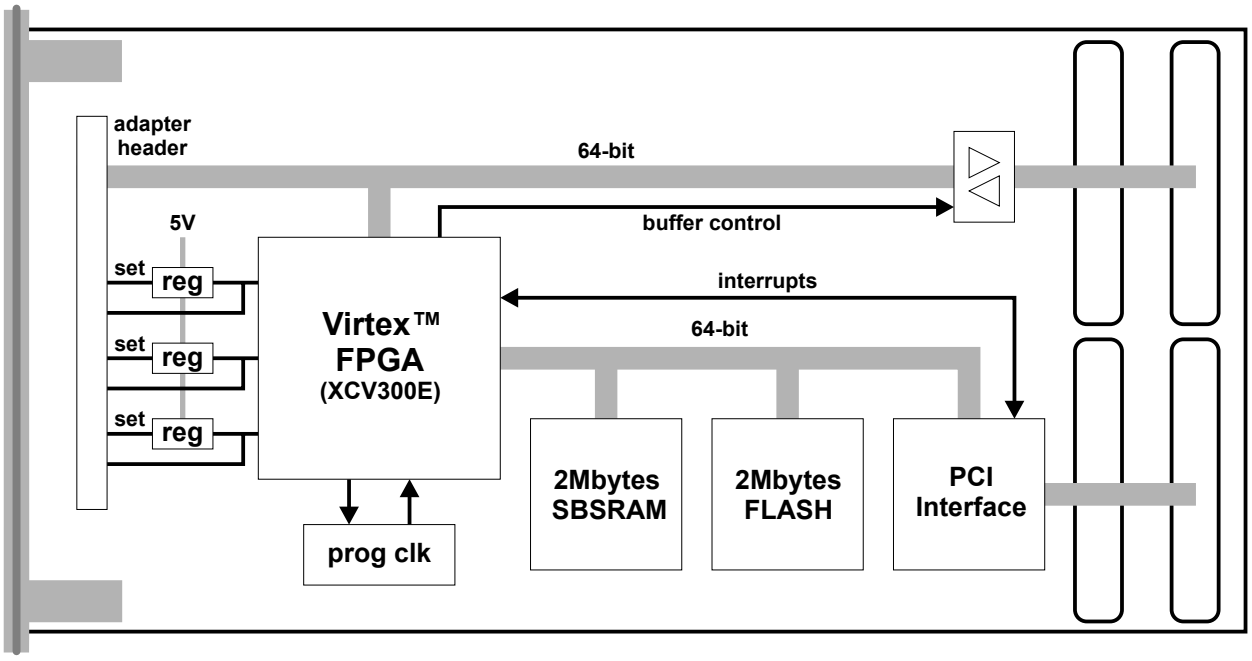
2Mbytes FLASH

The PMC-FPGA01 is a Xilinx XCV300E Virtex FPGA based digital I/O module aimed at high-bandwidth data I/O and ideal for custom interfaces. Typical interfaces include FPDP, LVDS, link port conversion, digital camera interface. This makes the PMC-FPGA01 ideal for imaging and radar applications.

TRANSTECH™ DSP

www.transtech-dsp.com

Block Diagram



Technical Specification

FPGA

Device	Xilinx Virtex XCV300E (contact Transtech for other FPGAs)
Package	FP BGA256
FPGA boot	Programmable via PMC/PCI interface

Memory

SBSRAM	2Mbytes
SDRAM	128Mbytes
Local Bus	64 bit, 80MHz
FLASH	2Mbytes

PCI

Device	QL5064
Compliance	64-bit, 66MHz PCI 2.2 Master/Slave/ DMA
Enhancements	Endian conversion, DMA, interrupt support
Bandwidth	up to 528Mbytes/sec

Environmental

Signalling	3.3/5V
Operating Temp	0° to 50°C
Power Dissipation	Programming dependent
Relative Humidity	5% - 90% (non-condensing)

Software Support

Runtime Support	Windows 98, Windows NT4/2000, VxWorks
Libraries	Application Library in C++, includes full source code
Examples	Host C++ and FPGA VHDL code for DMAs, Interrupt handling and FPDP
Utilities	Load FPGA and Flash, test and probe system hardware.

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